

FIG.1

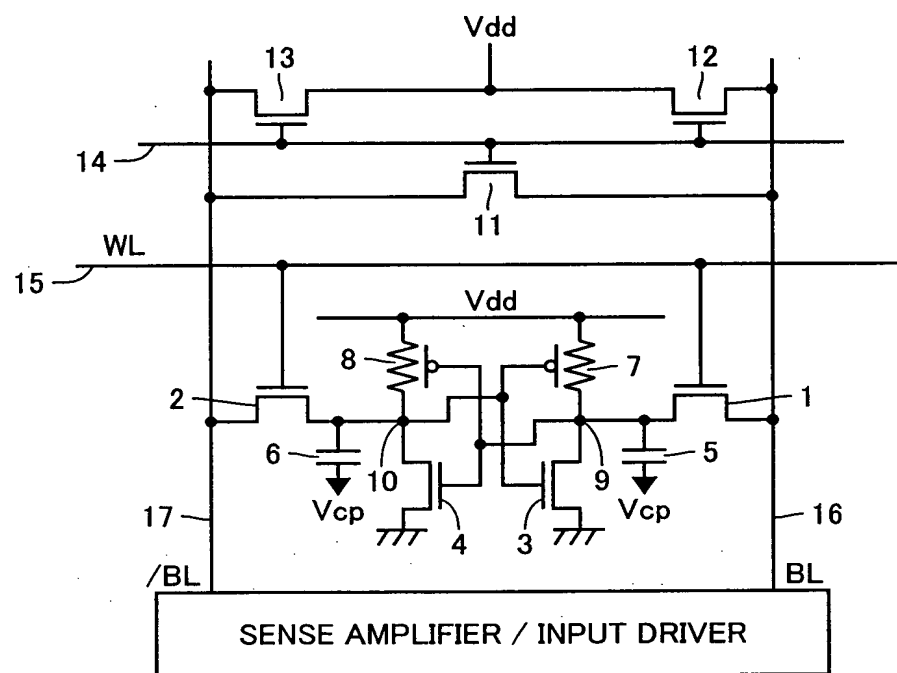


FIG.2

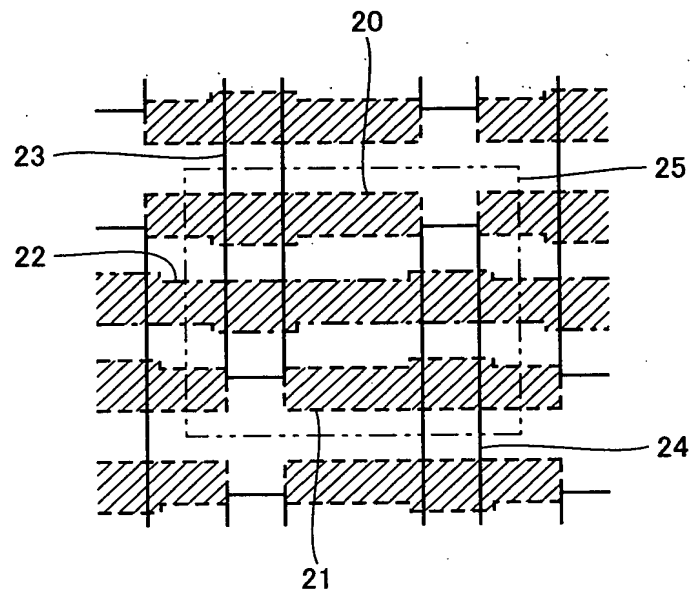


FIG.3

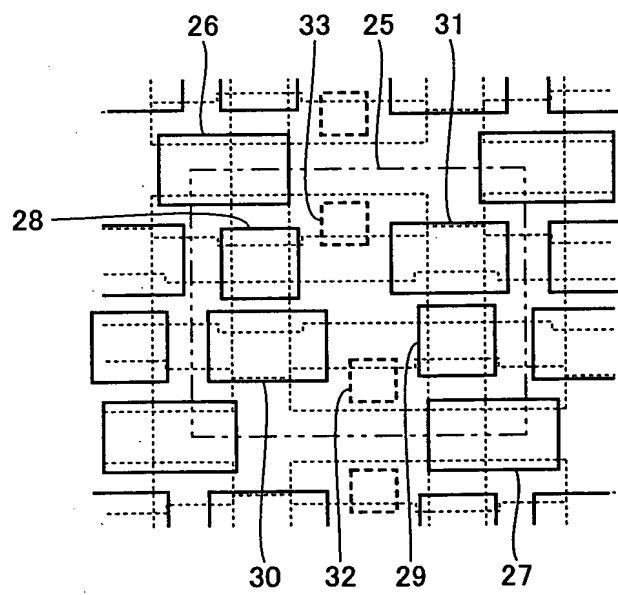


FIG.4

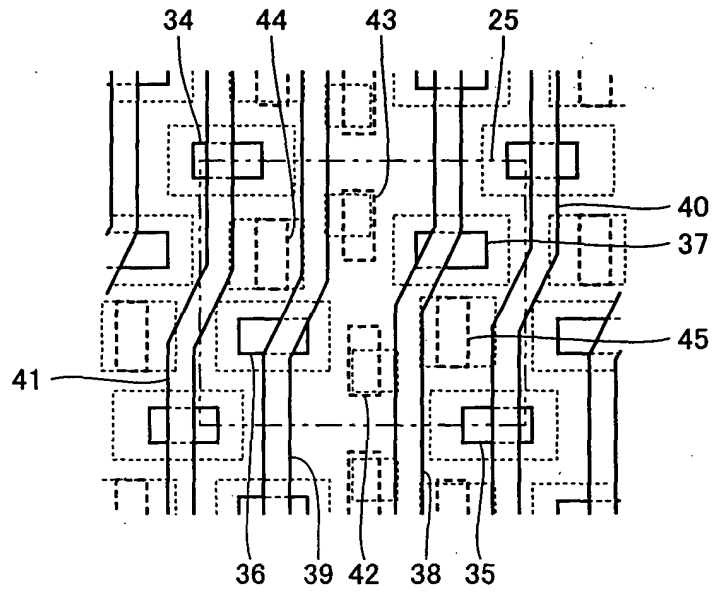


FIG.5

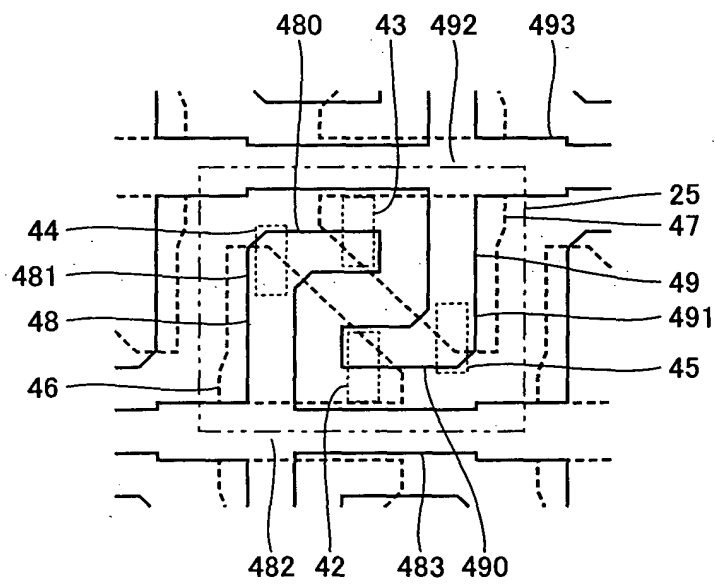


FIG.6

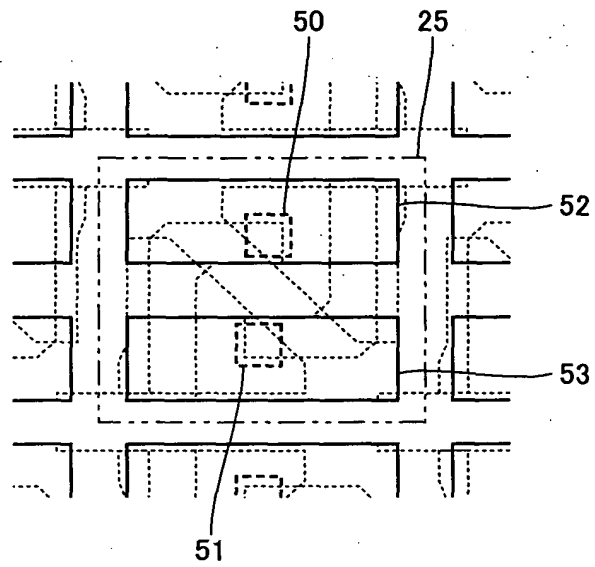
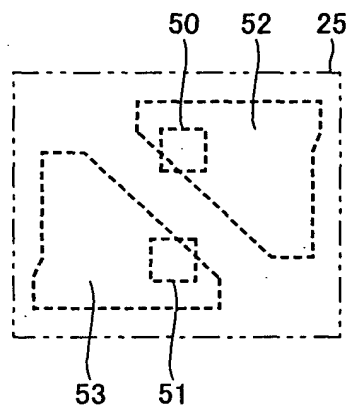


FIG.7



60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87

FIG.9

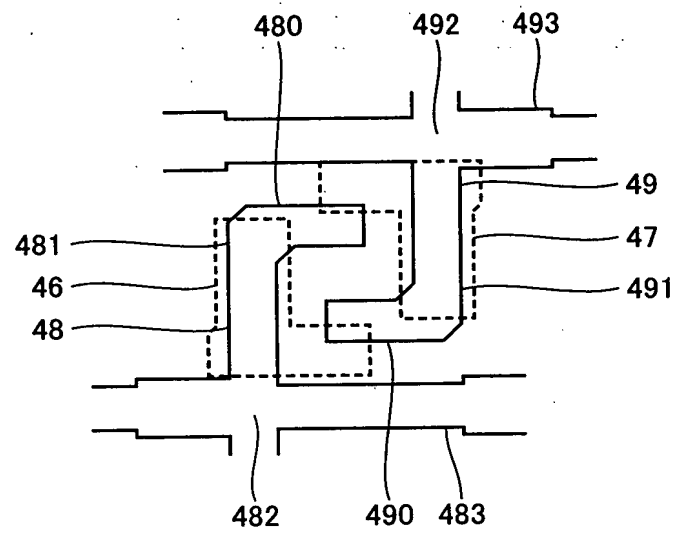


FIG.10

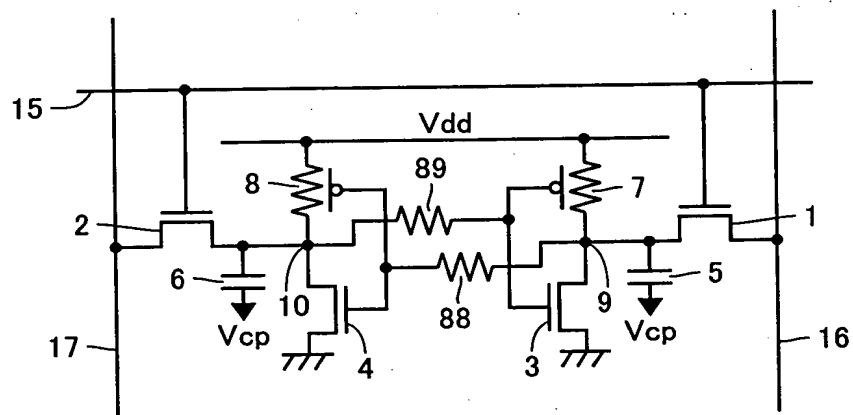


FIG. 11

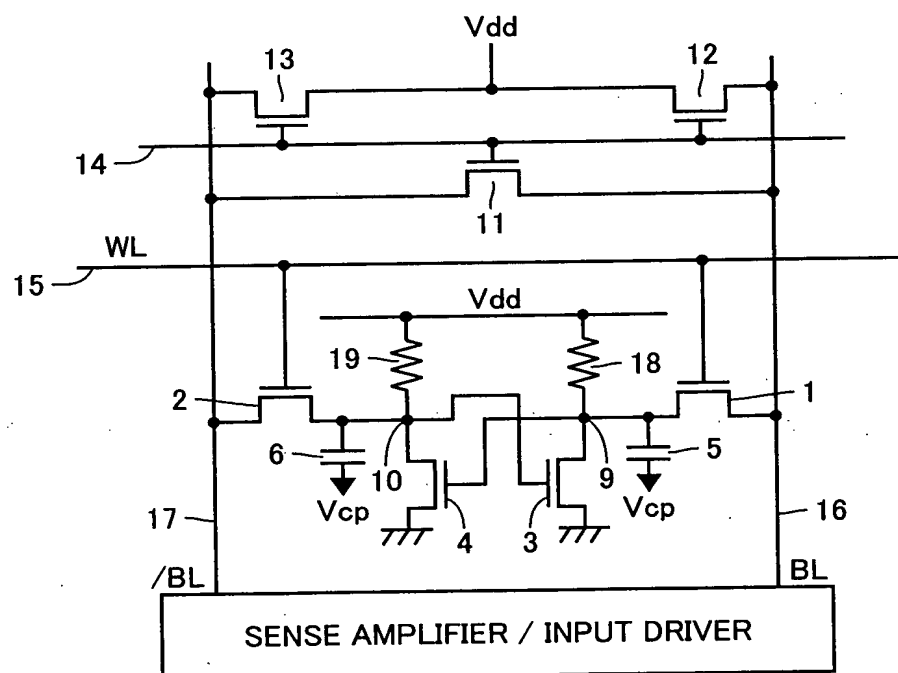


FIG.12

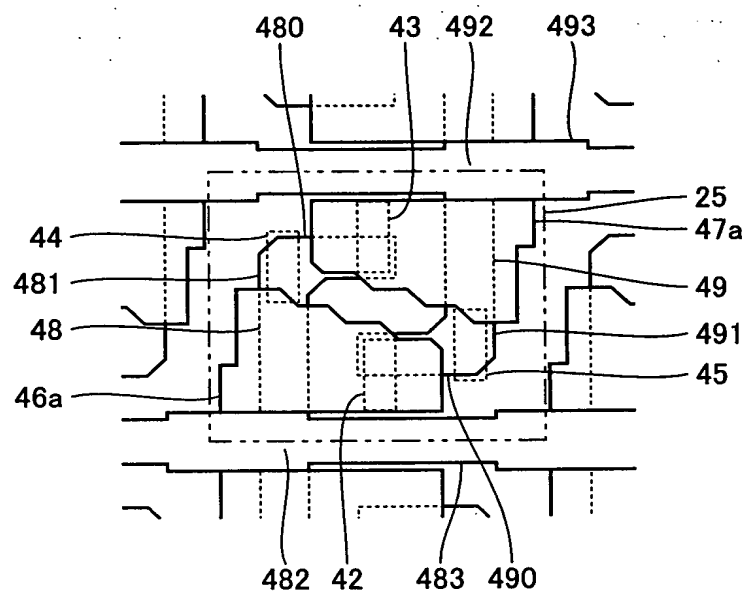


FIG.13

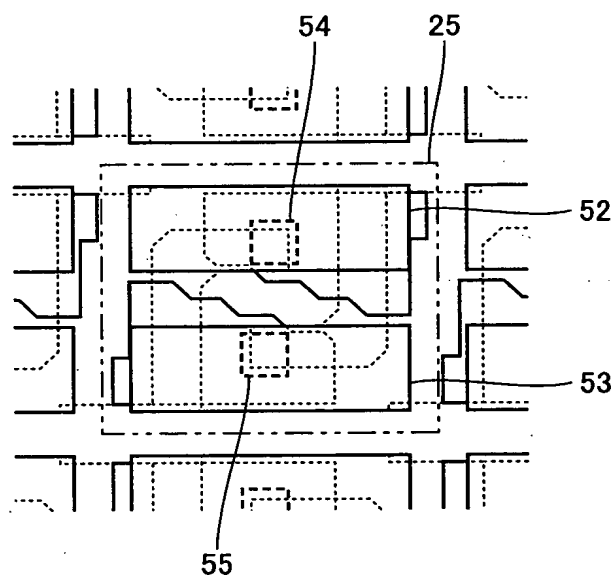




FIG.14

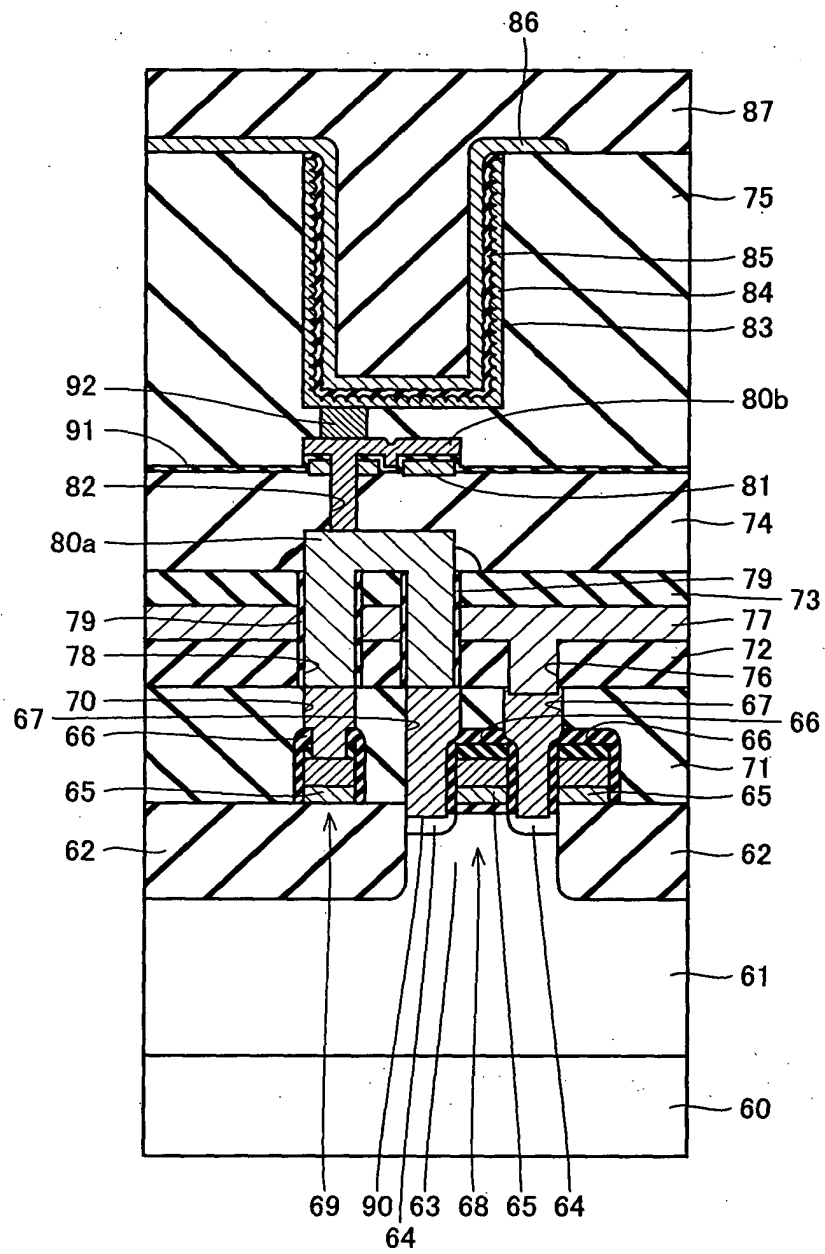


FIG.15

MAXIMUM POTENTIAL  
OF "L LEVEL"  
MEMORY NODE (V)

